## Features:

- High Frequency operation.
- 0-360degrees of phase shift @ $56-70 \mathrm{GHz}$.
- Low insertion loss of $<1 \mathrm{~dB} @ 56-70 \mathrm{GHz}$
- Amplitude Variation less than $\pm 1.5 \mathrm{~dB}$
- Control Voltage:Digitalcontrol voltage varying from $+3.3 \mathrm{Vto}-3.3 \mathrm{~V}$


## Description:

RFICPSO7 is an Digital phase shifter with control voltage varrying from +3.3 to -3.3 V . RFICPSO7 Phase Shifter provides phase shift of 0 to 360 degrees from $56-70 \mathrm{GHz}$ with minimum insertion loss delta of 3 dB . The technology used to design phase shifter is 0.1 um GaAs Phemt process. This phase delta varies with frequency.

The phase shift is with respect to the control voltage.The low insertionloss and compact size enable this part to be usedin a wide range of applications, including the phase adjustment of clocks in fiber optic systems, phase array system, Military application and testequipment.

## Applications

- Wireless 5G Systems
- Supports Phased array antenna system.
- Satellite Communication
- TDD/FDD System.


## Functional Block Diagram



## Pin Configuration:

| Pin No. | Pin name | Description |
| :---: | :---: | :---: |
| 1 | GND | RF Ground |
| 2 | PS_IN | Input pin of Phase Shifter |
| 3 | GND | RF Ground |
| 4 | A1 | control Voltage |
| 5 | A2 | control Voltage |
| 6 | A3 | control Voltage |
| 7 | A4 | control Voltage |
| 8 | A5 | RF Ground |
| 9 | GND | RS_OUT | | Output pin of Phase Shifter |
| :---: |
| 10 |

## Circuit diagram of 4-bit Digital Phase Shifter



## Electrical Specification:

Freq $=56-70 \mathrm{GHz}$, Digital Bit Controlvoltage sweep: +3.3 V to $-3.3 \mathrm{~V}, \mathrm{Z}_{0}=50 \Omega$.

| Parameter | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: |
| Insertion Loss | - | -10 | -12.5 | dB |
| Input Return Loss | - | -10 | - | dB |
| Output Return Loss | - | -10 | - | dB |
| Phase Shift Delta | - | 360 | - | deg |
| Insertion Loss Variation | - | 0.400 | - | dB |
| Control Voltage Current |  |  |  |  |

## Typical Performance Curves




Input Return Loss


Output Return Loss


PRE-RELEASE DATASHEET

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